

**Amendments to the Specification:**

Please replace the paragraph beginning at page 13, line 20, spanning to page 14, line 3 with the following amended paragraph:

As seen from the following Table 1 and Fig. 10, a condition in which more than 95% of the video signal VD can be charged for this pixel (PIX(768,3072)) within a charge time of about 10  $\mu$ s is satisfied where the organic protective film 48 has a thickness (d) of 0.8  $\mu$ m or less and a dielectric constant ( $\epsilon$ ) of less than ~~[[2]]~~ 3.0 and a case where the organic protective film 48 has a thickness (d) of 1.3  $\mu$ m or 1.5  $\mu$ m and a dielectric constant ( $\epsilon$ ) of less than 4.0.

Please replace the paragraph beginning at page 14, line 7 with the following amended paragraph:

Also, the video signal VD can be sufficiently charged rapidly in a charge time (i.e., 9.3  $\mu$ s) when a thickness (d) of the organic protective film 48 ~~is~~ 0.9  $\mu$ m and a dielectric constant ( $\epsilon$ ) thereof is 3 in accordance with a timing margin between gate pulses. A thickness of the organic protective film 48 is limited to at most 1.5  $\mu$ m in consideration of the coating uniformity and the etching uniformity in the organic protective film 48 as mentioned above.

Please replace the paragraph beginning at page 15, line 5 with the following amended paragraph:

In the table 2, parasitic capacitance values are values measured when a thickness of the organic protective film 48 coated on the data line ~~[[54]]~~ 52 is 1.25 $\mu$ m and an area (A) of the overlapping line 56a at which the pixel electrode 50 is overlapped with the data line 52 is 837 $\mu$ m<sup>2</sup>. The area (A) is a case where a length in the longitudinal side of the pixel cell

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(PIX(768,3072)) is  $279\mu\text{m}$  and a width of the overlapping line 56a at which the pixel electrode 50 is overlapped with the data line 52 is  $3\mu\text{m}$ . On the other hand, the parasitic capacitance of the overlapping line 56b where the pixel electrode 50 is overlapped with the gate line 54 is ~~[[less]]~~ greater than the parasitic capacitance of the overlapping line 56a because a thickness of the organic protective film 48 is ~~thicker~~ thinner than that between the pixel electrode 50 and the data line 52.